

ABSTRACT

A radio transceiver comprises a reception path, a transmission path, and a frequency generator with a programmable phase lock loop. The reception path, the transmission path, and the frequency generator share a maximum amount of common circuitry to facilitate implementation of the entire radio transceiver on a single integrated circuit. The reception path includes an amplifier and a quadrature mixer for producing low intermediate frequency signals. The transmission path can be controlled by either a modulated voltage controlled transmitter or an in-phase and quadrature modulator transmitter.

0921555-142200